

**REPLACEMENT
SHEET**



00R00681

1 / 20

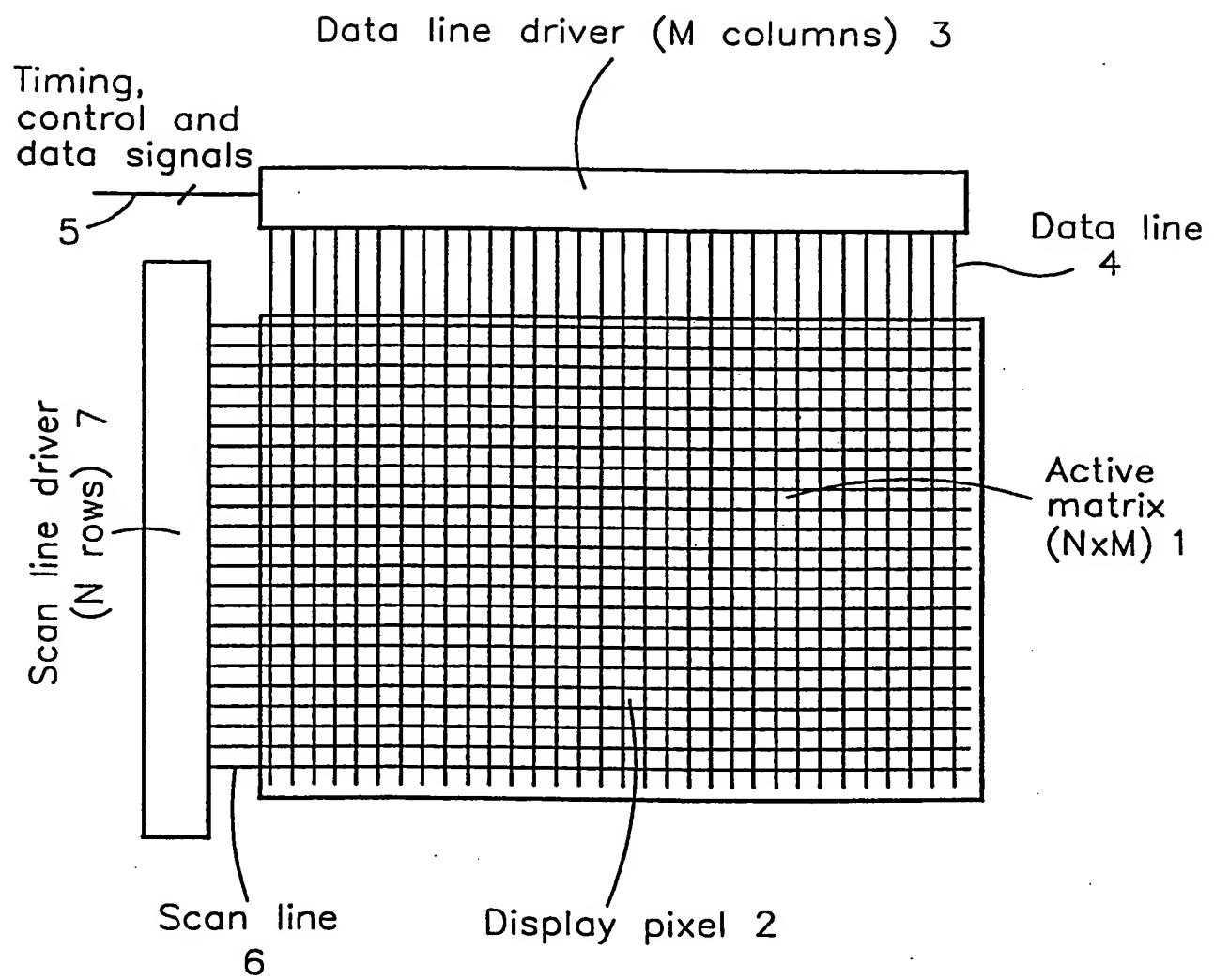


FIG 1

00R00681

2 / 20

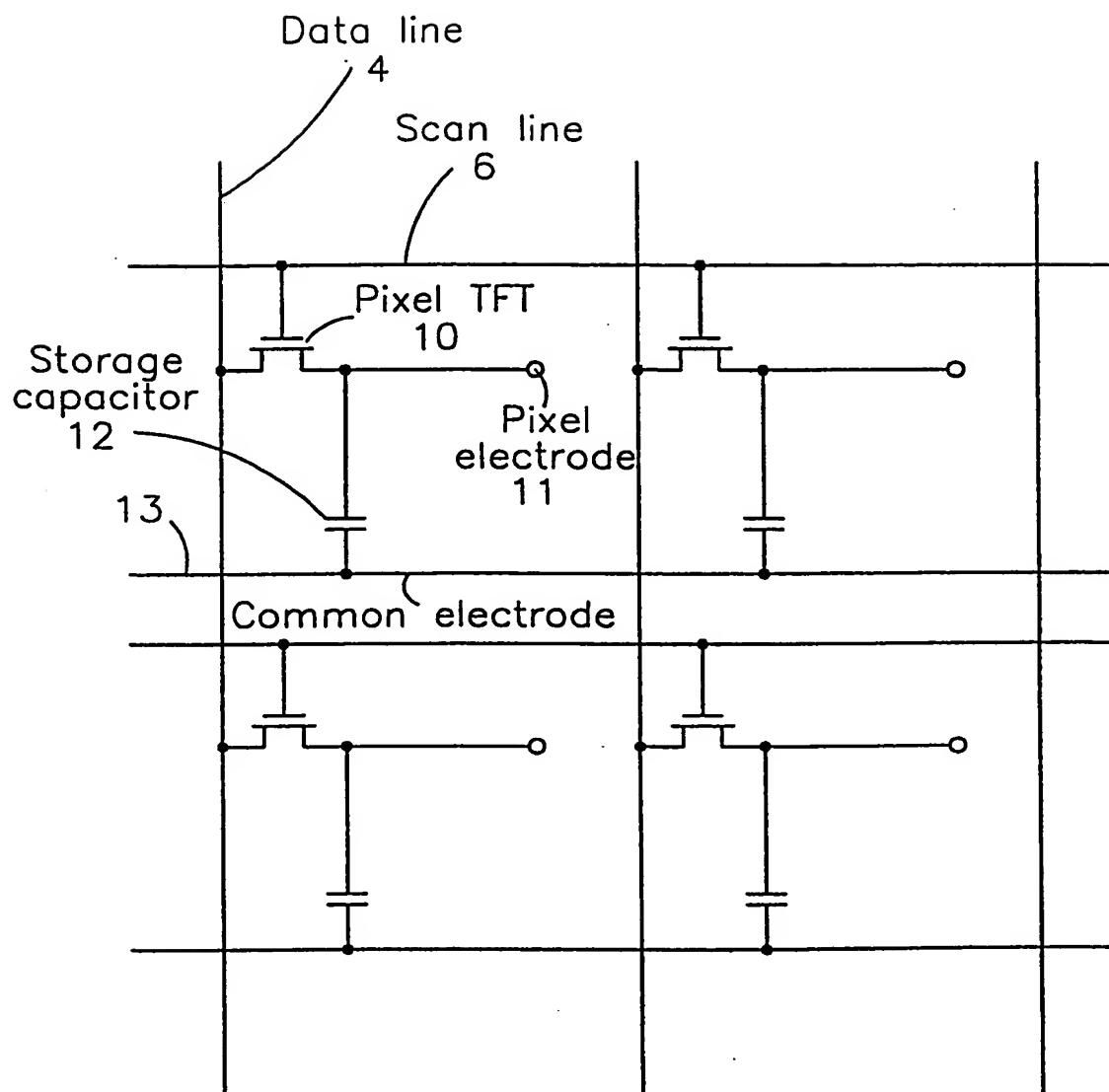


FIG 2

REPLACEMENT SHEET

00R00681

3 / 20

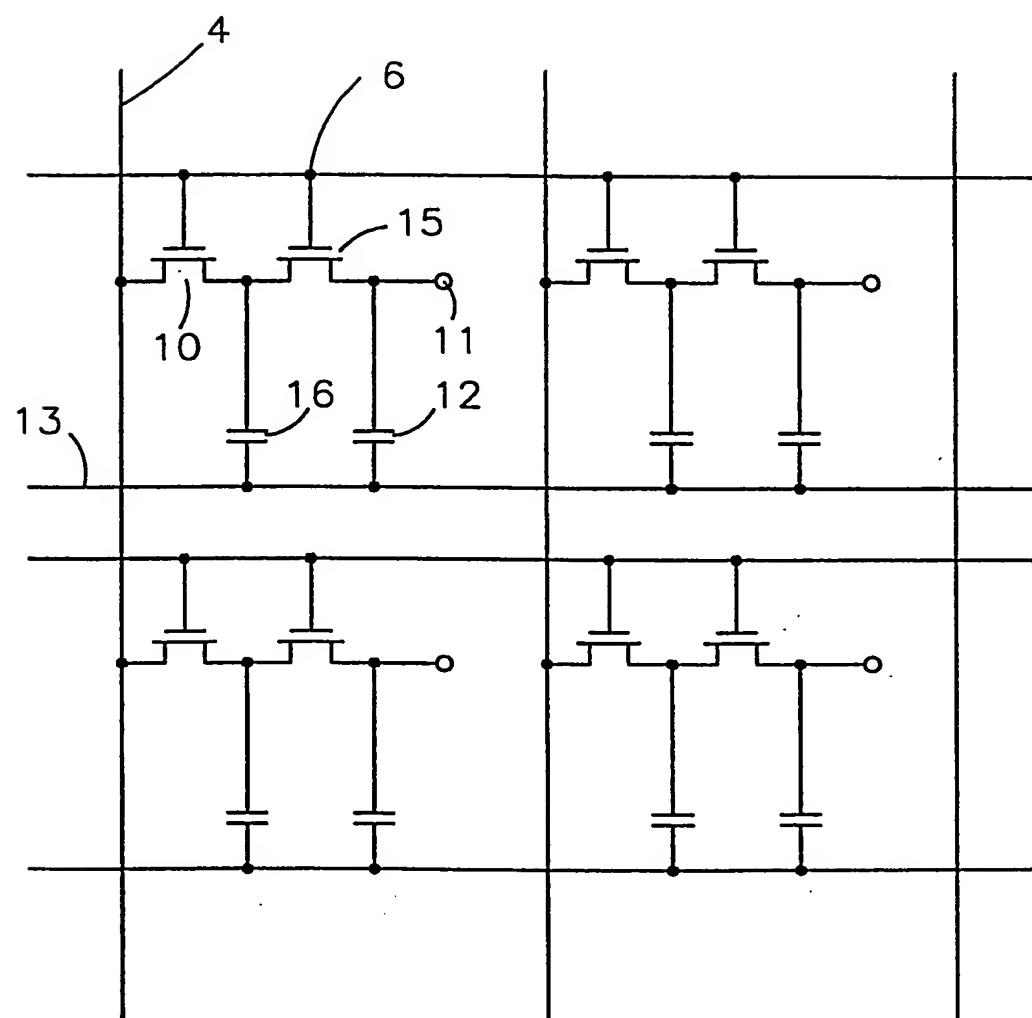


FIG 3

**REPLACEMENT
SHEET**

00R00681

4 / 20

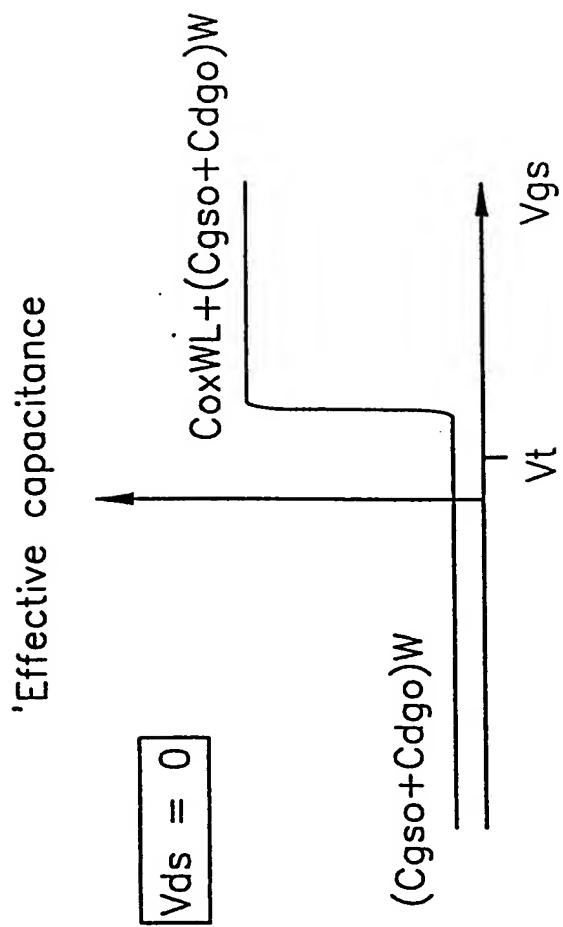
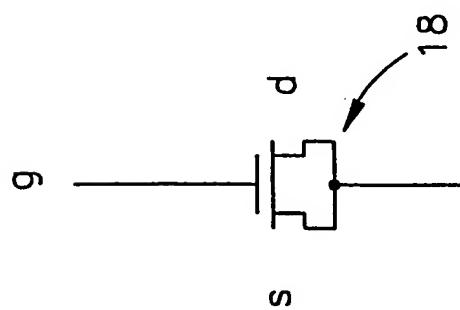


FIG 4



**REPLACEMENT
SHEET**

00R00681

5 / 20

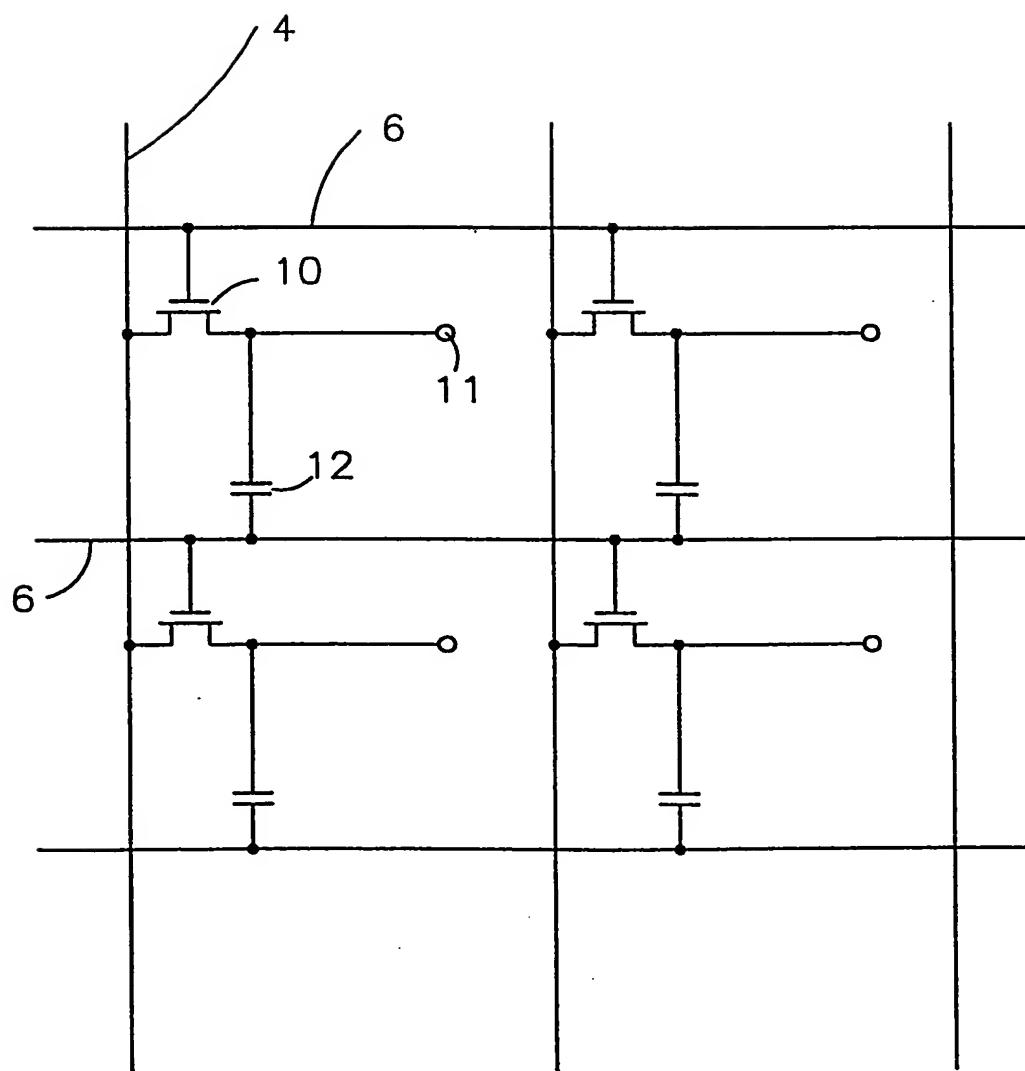


FIG 5

**REPLACEMENT
SHEET**

00R00681

6 / 20

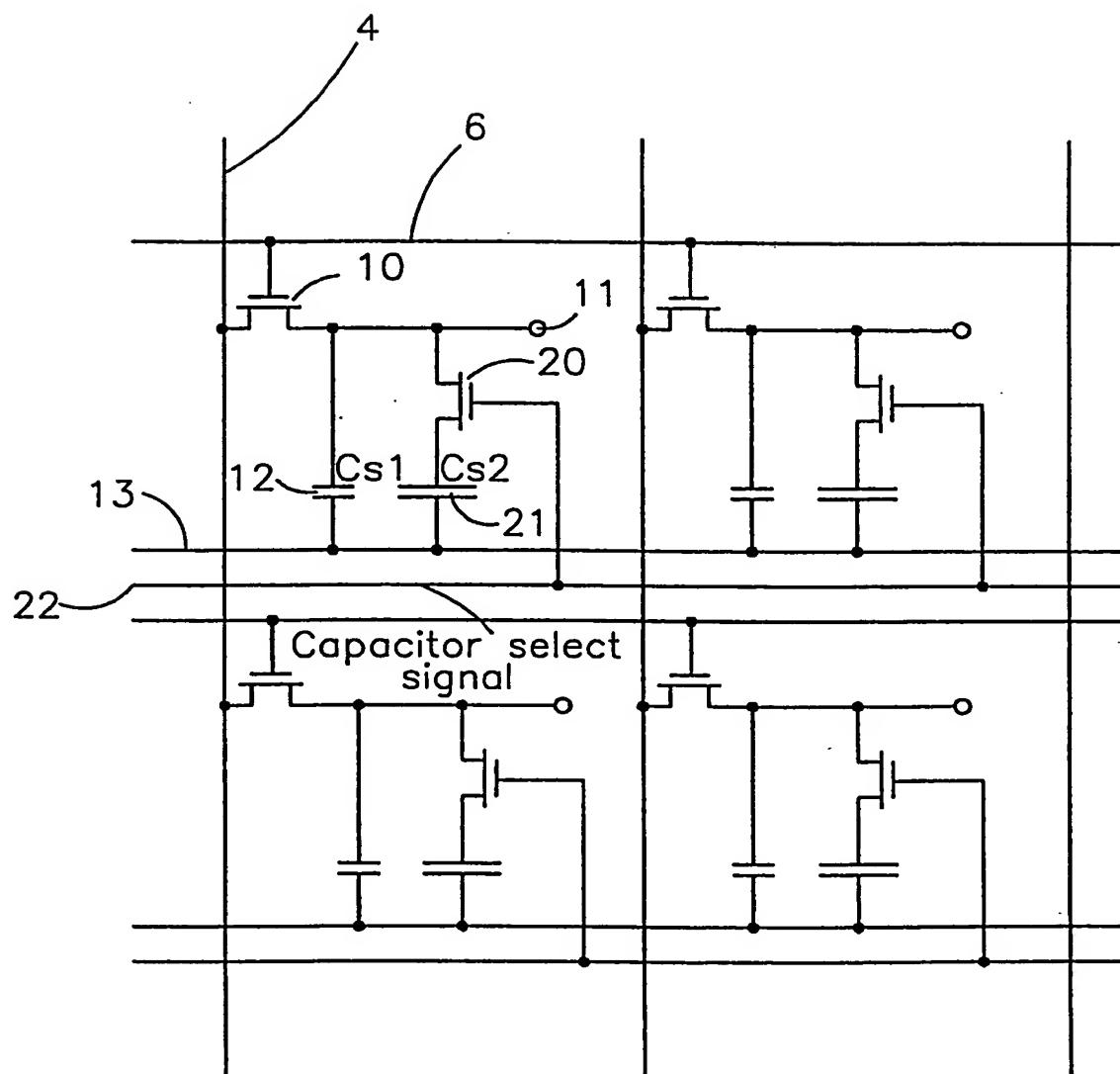
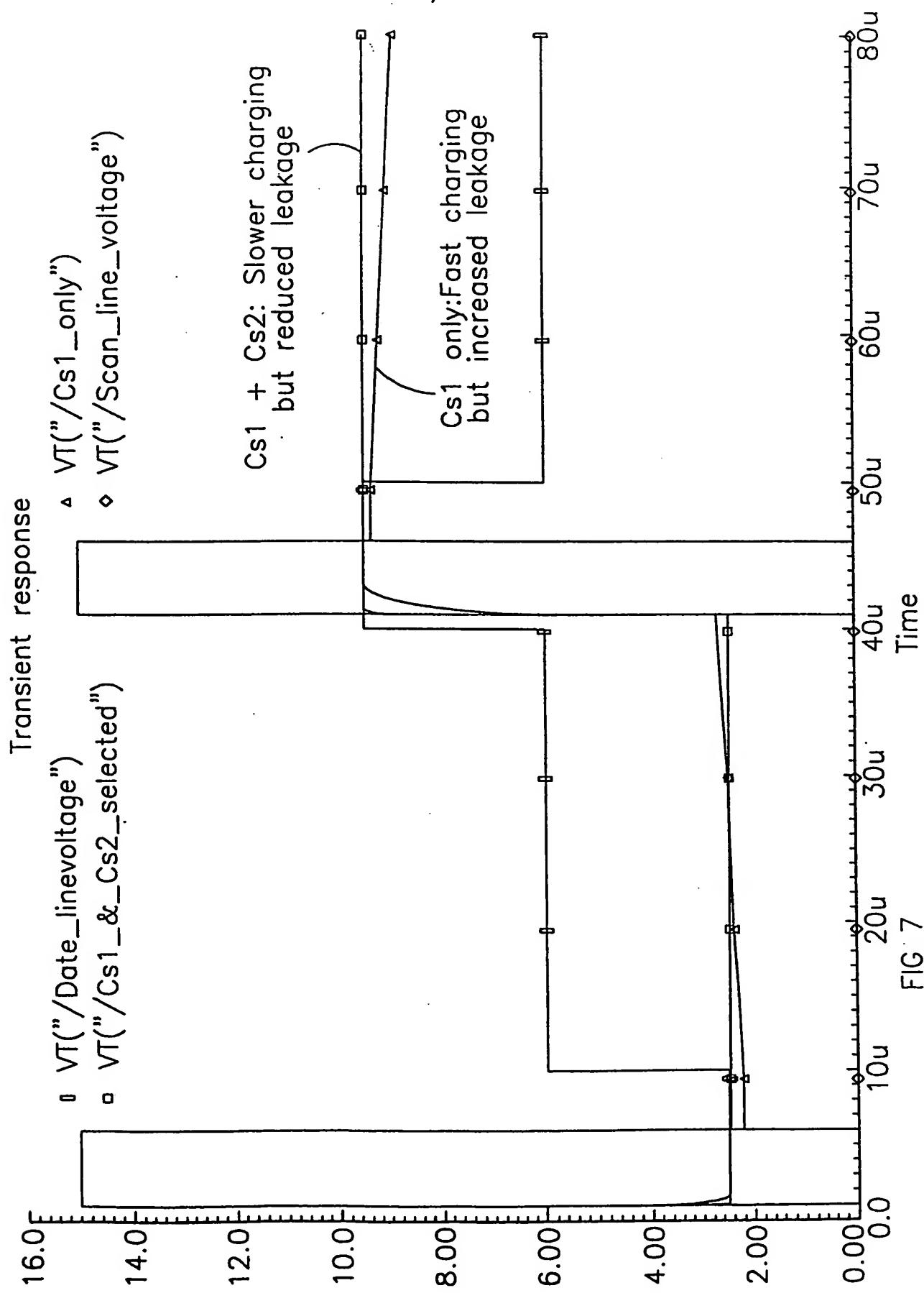


FIG 6

REPLACEMENT SHEET

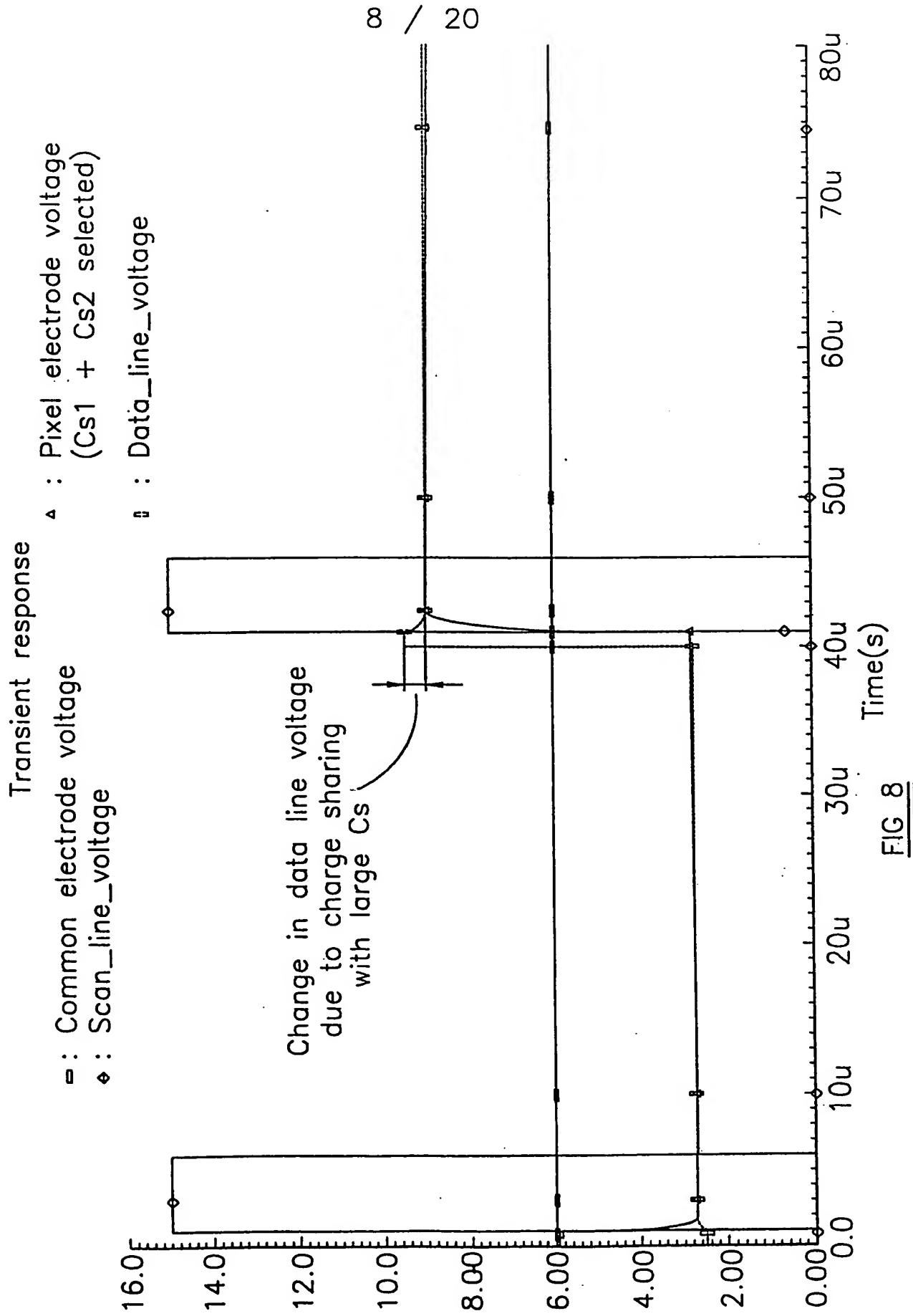
00R00681

7 / 20



REPLACEMENT
SHEET

00R00681



**REPLACEMENT
SHEET**

00R00681

9 / 20

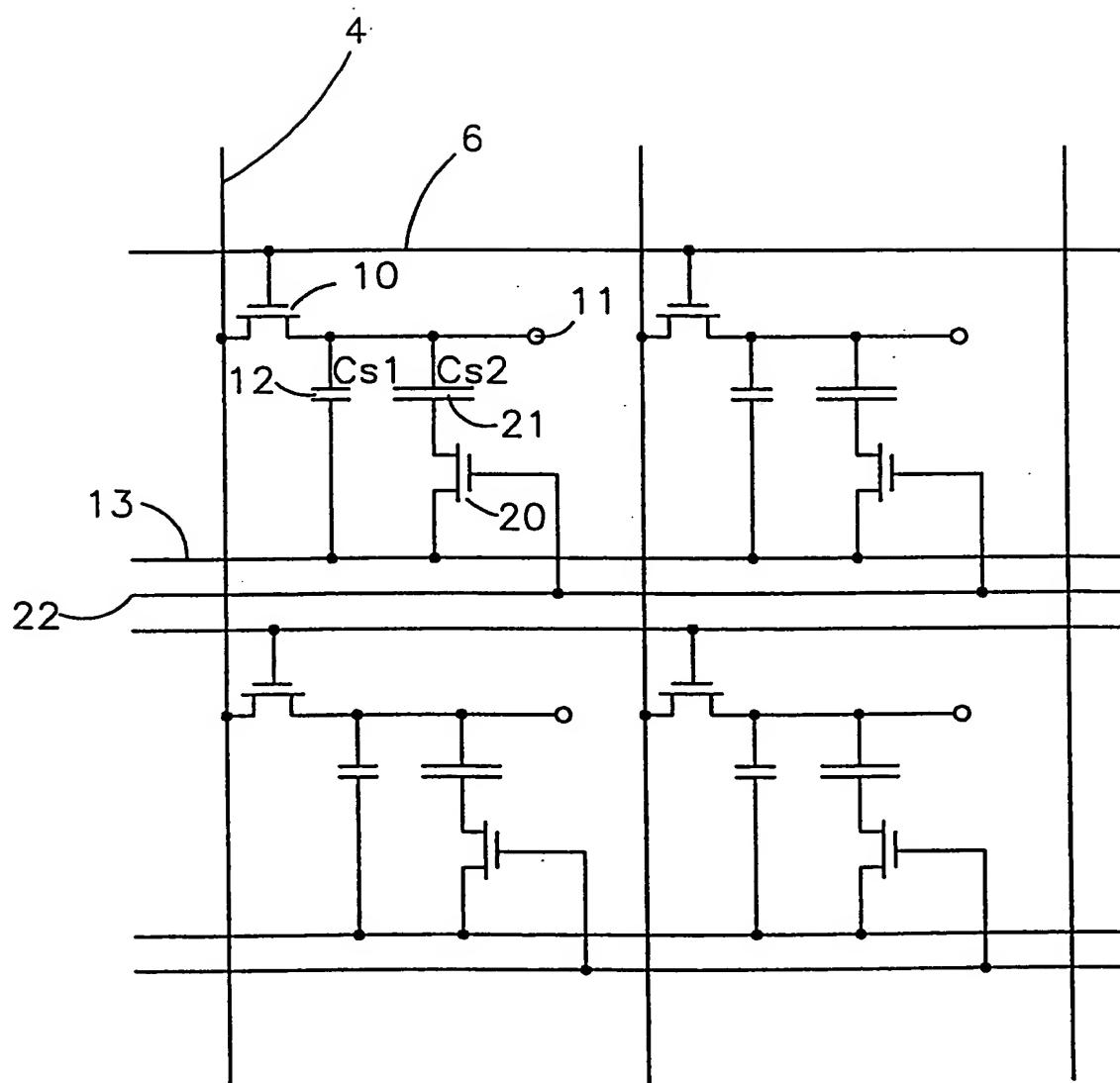


FIG 9

**REPLACEMENT
SHEET**

00R00681

10 / 20

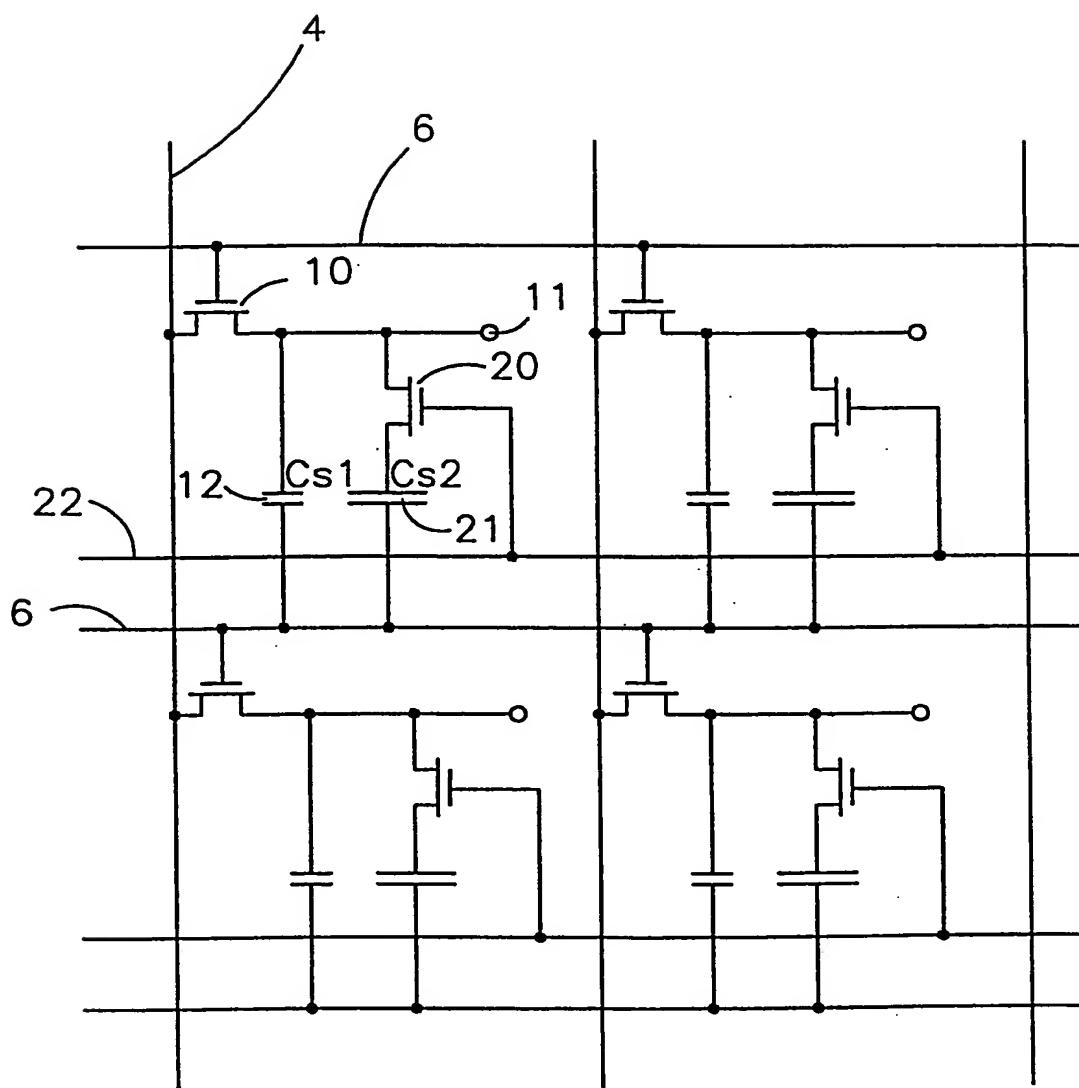


FIG 10

**REPLACEMENT
SHEET**

00R00681

11 / 20

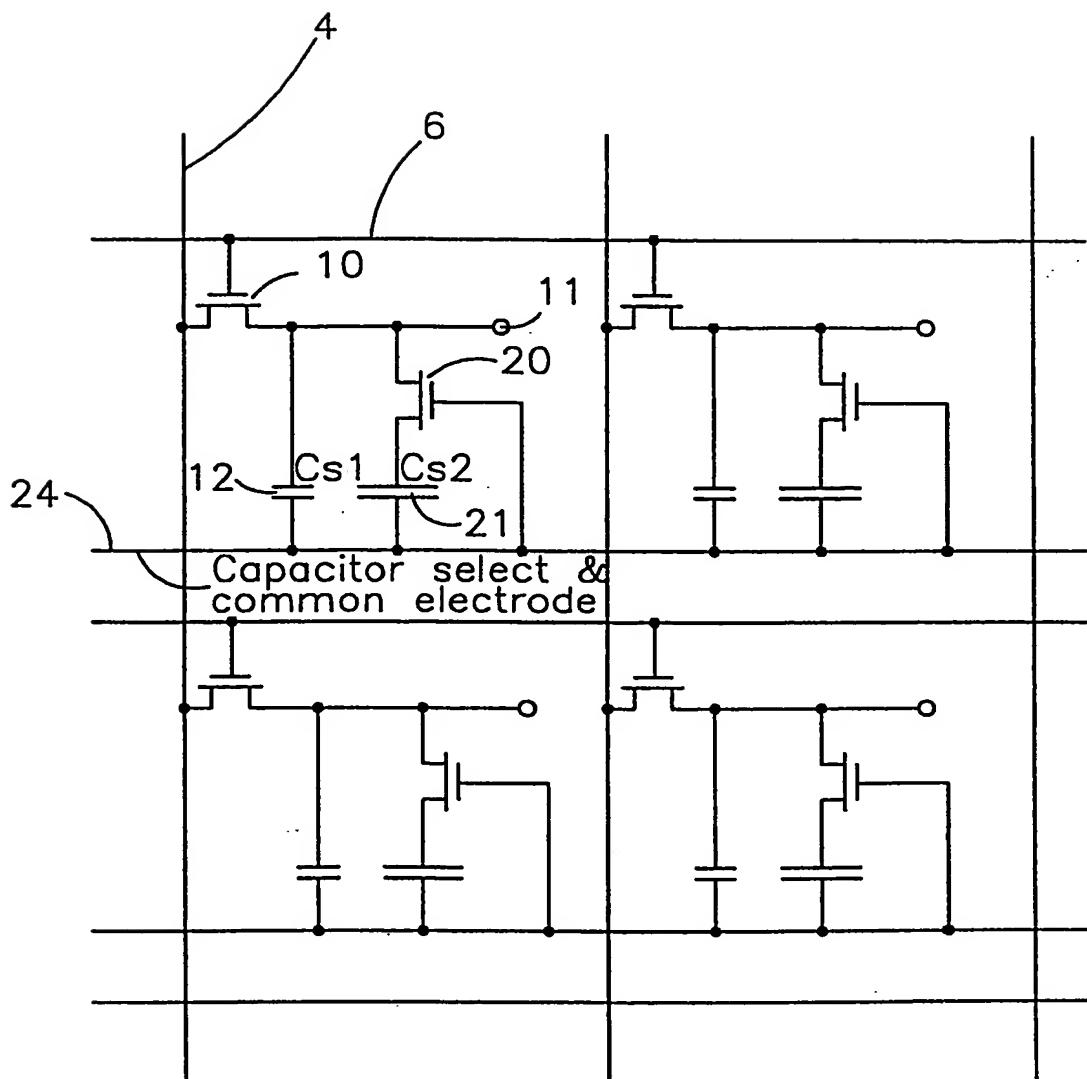


FIG 11

**REPLACEMENT
SHEET**

00R00681

12 / 20

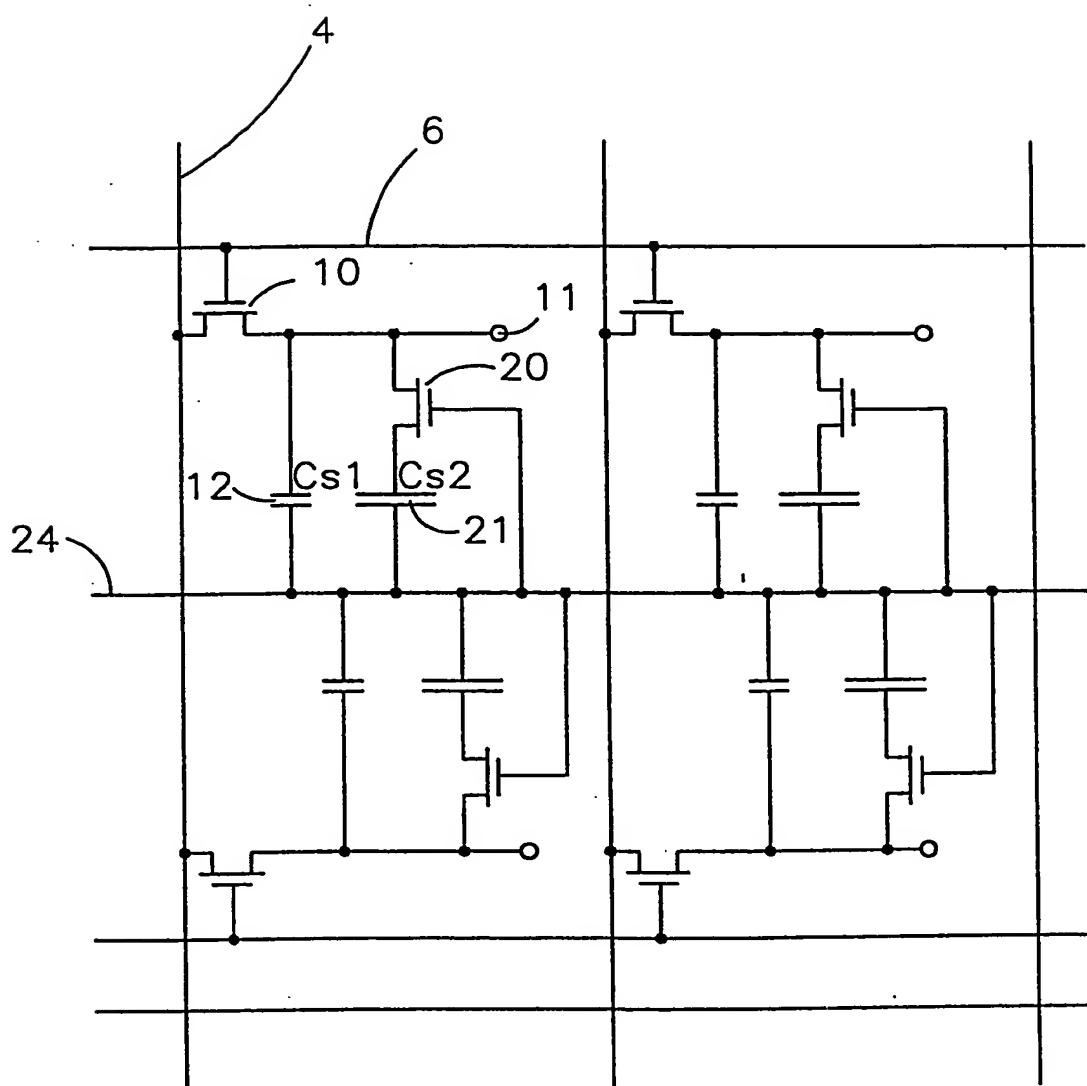


FIG 12

**REPLACEMENT
SHEET**

00R00681

13 / 20

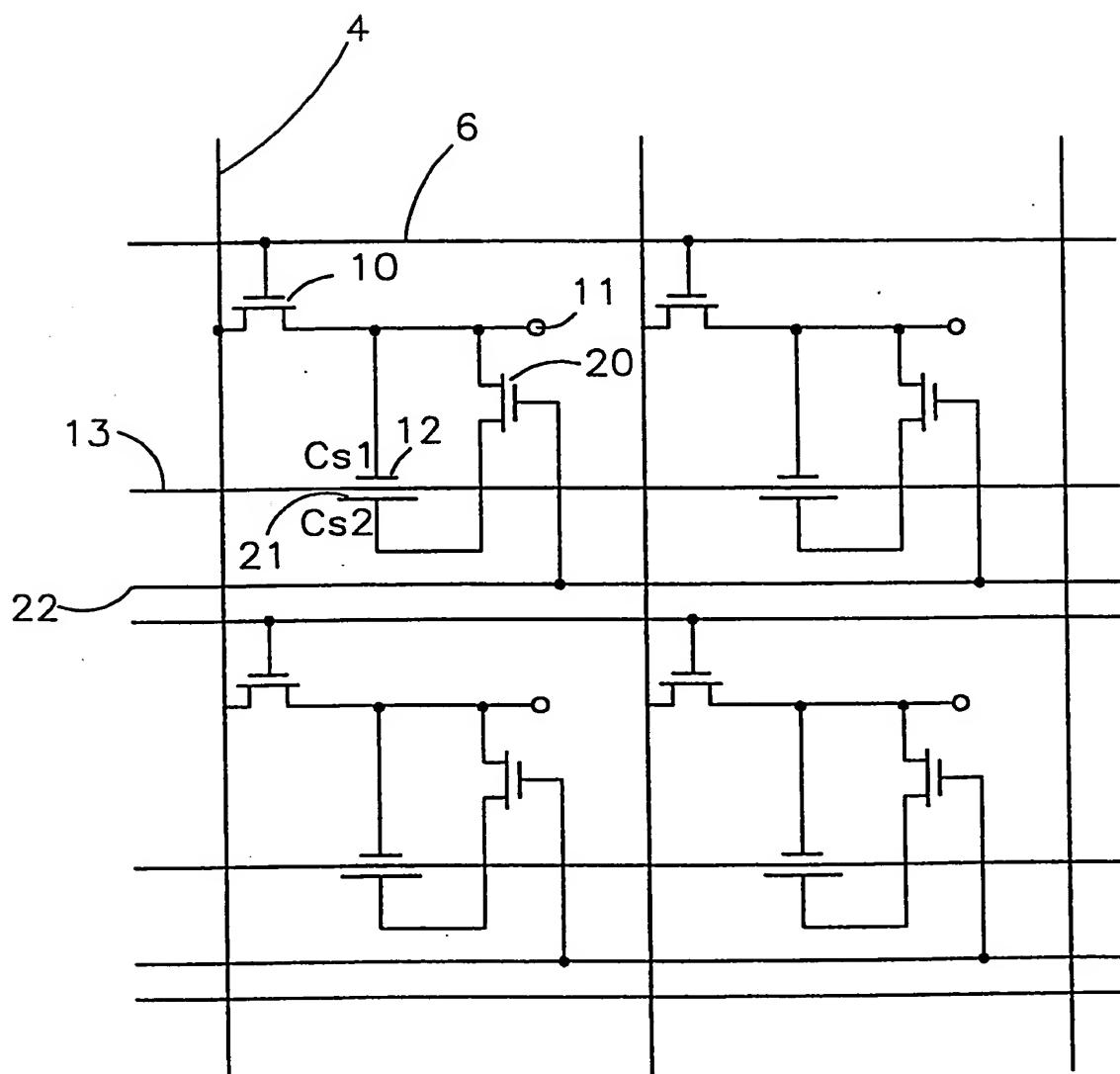


FIG 13

REPLACEMENT SHEET

00R00681

14 / 20

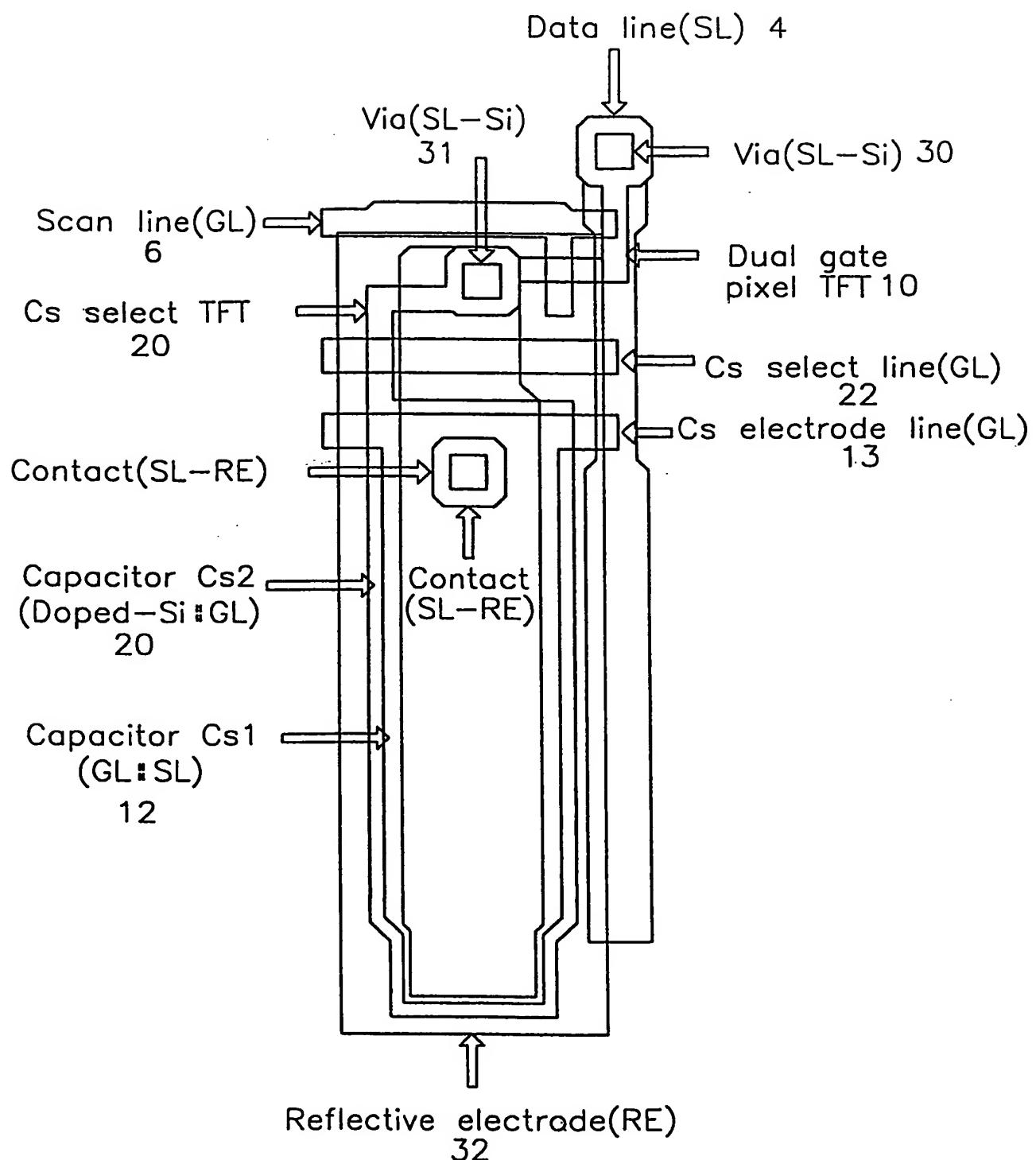


FIG 14

**REPLACEMENT
SHEET**

00R00681

15 / 20

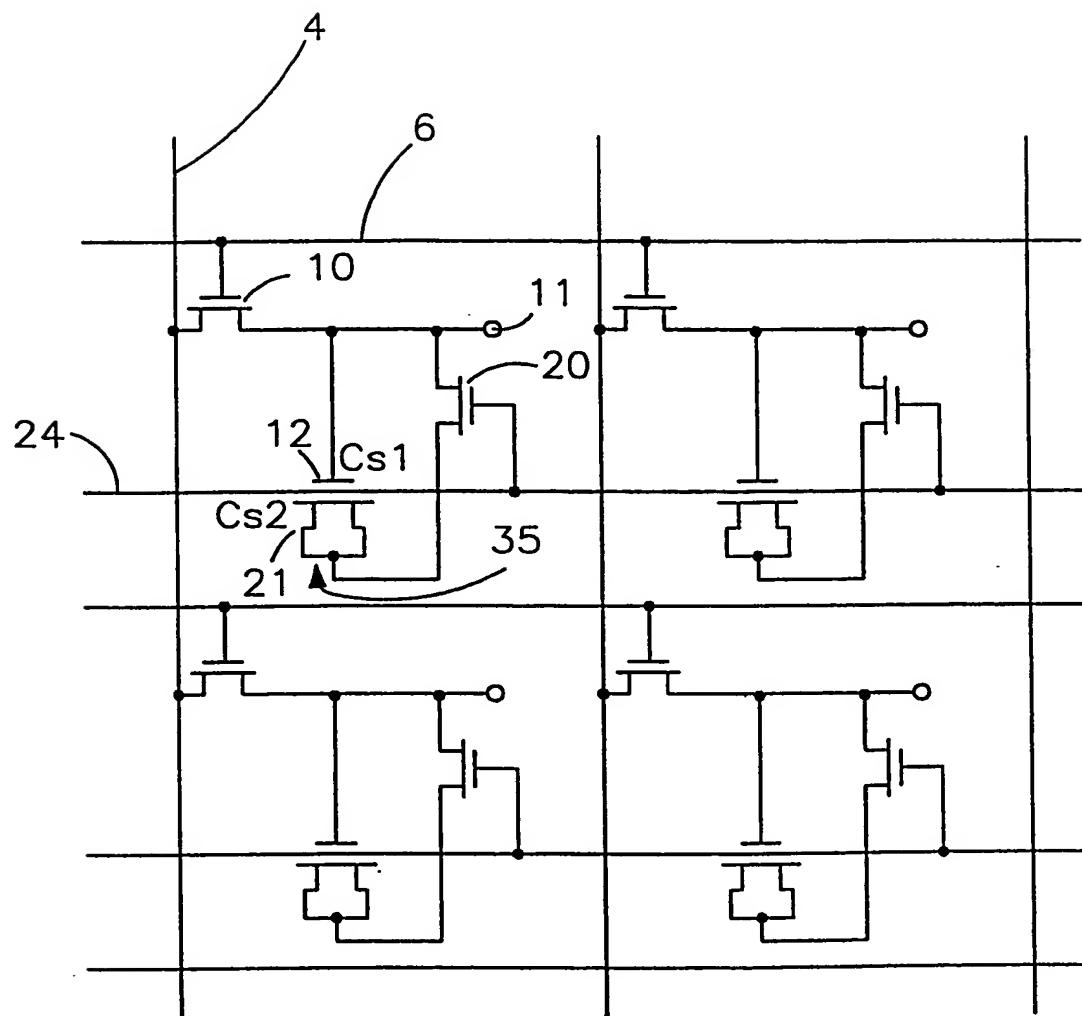


FIG 15

REPLACEMENT SHEET

00R00681

16 / 20

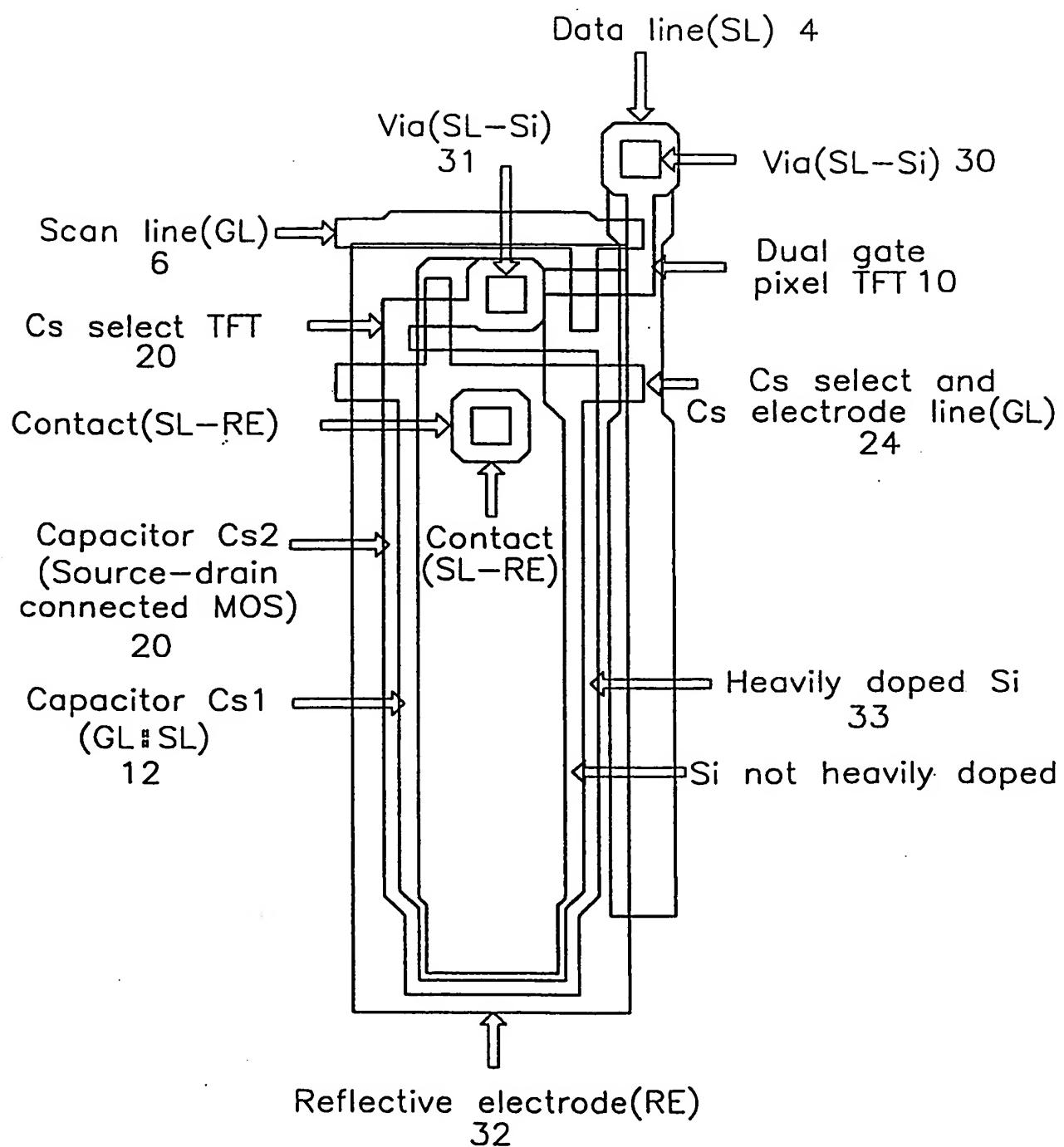
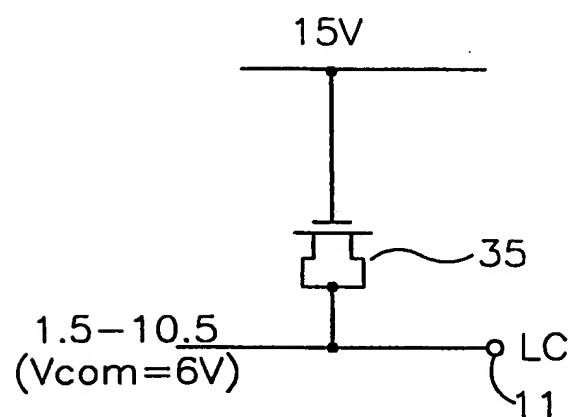


FIG 16

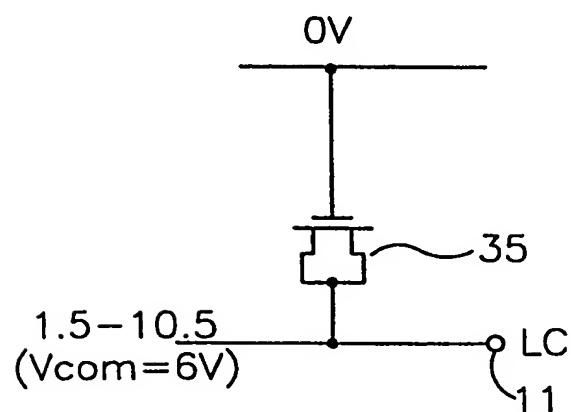
**REPLACEMENT
SHEET**

00R00681

17 / 20



Condition for
high capacitance:
 $Vt < 4.5$



Condition for
low capacitance:
 $Vt > -1.5$

FIG 17

REPLACEMENT SHEET

00R00681

18 / 20

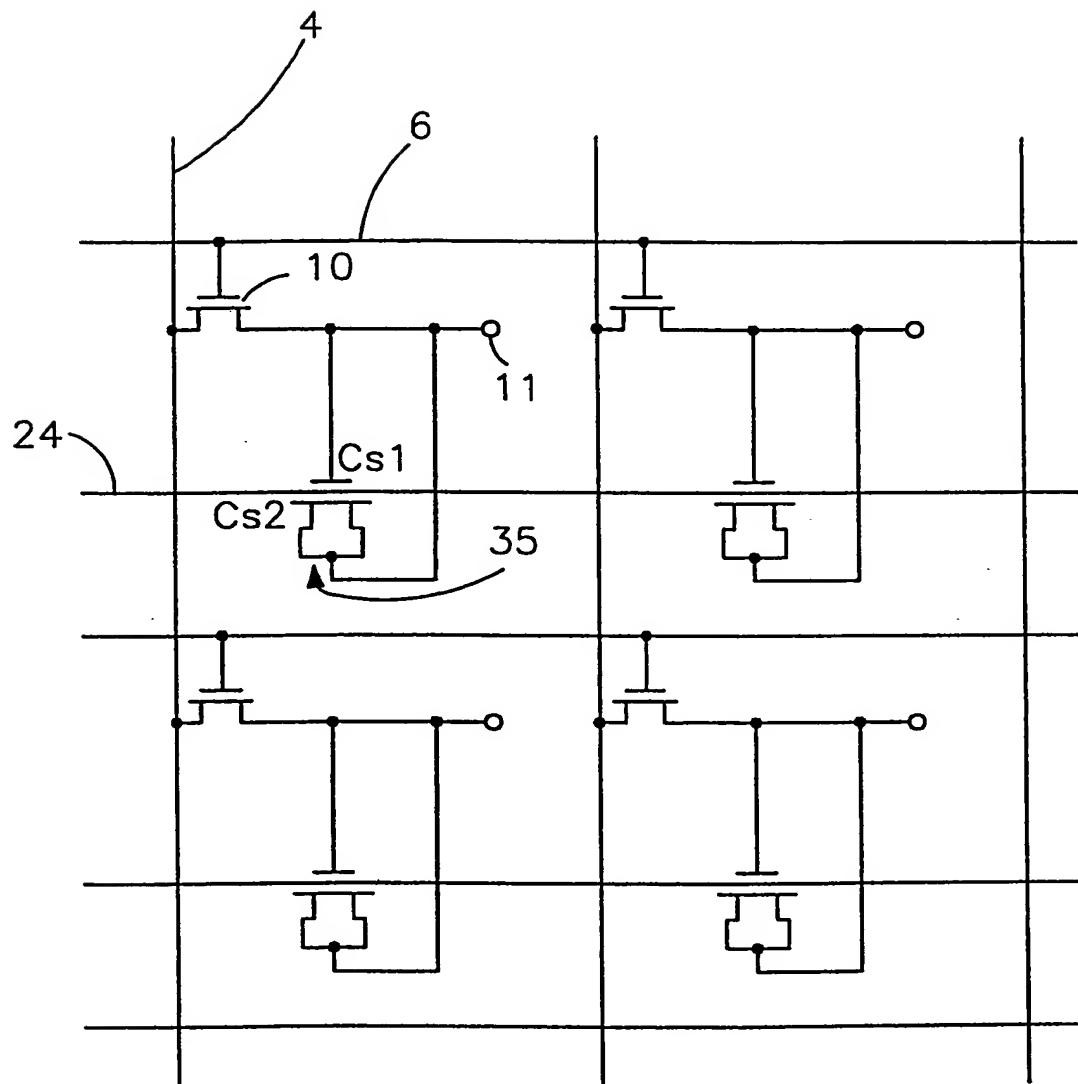


FIG 18

**REPLACEMENT
SHEET**

00R00681

19 / 20

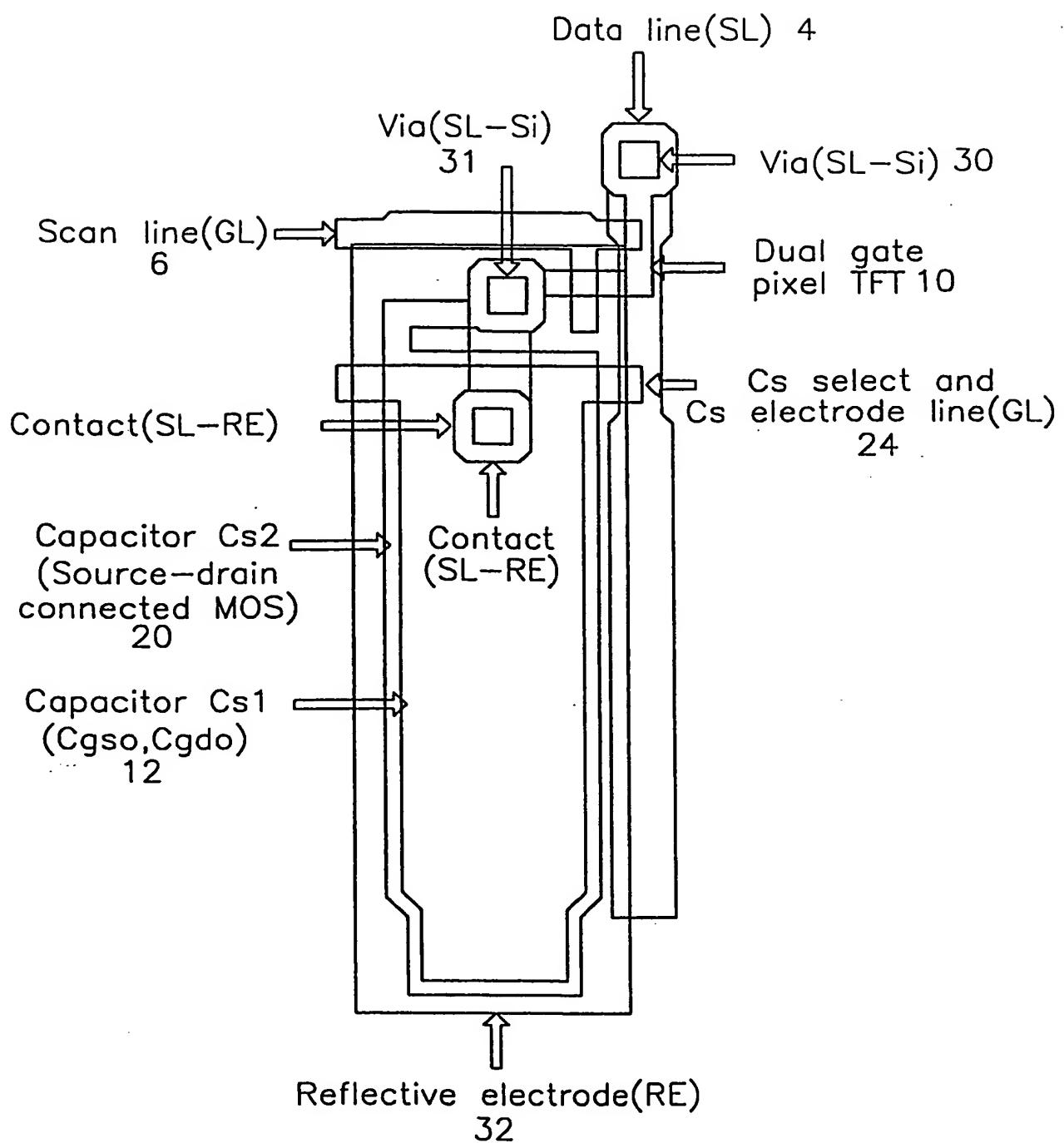


FIG 19

**REPLACEMENT
SHEET**

00R00681

20 / 20

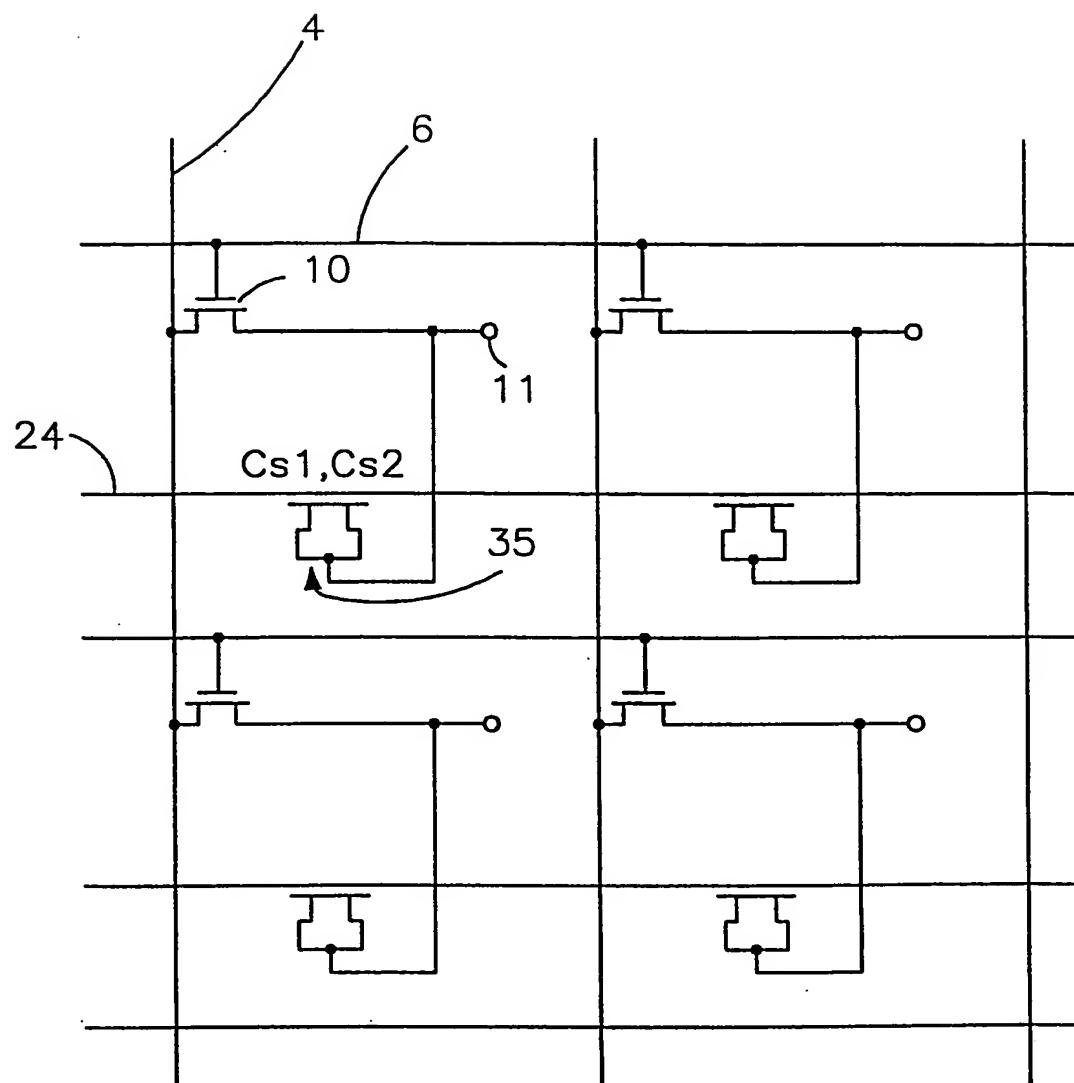


FIG 20